UNITED STATES PATENT APPLICATION

OF

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FOR

SYSTEMS AND METHODS FOR IMPLEMENTING SEGMENTATION IN SPEECH RECOGNITION SYSTEMS

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accuracy of the speech recognizer.

FIELD OF THE INVENTION

[001] The present invention relates generally to speech recognition systems and, more particularly, to systems and methods for improving the segmentation of acoustic data within speech recognition systems.

BACKGROUND OF THE INVENTION

[002] Speech recognition systems conventionally use phonemes to model speech. The duration of various phonemes in input speech utterances can be different, therefore, a conventional speech recognizer performs a segmentation process on the spoken utterance to divide the utterance into segments of speech, where each segment corresponds to a phonetic or sub-phonetic unit. A conventional speech recognizer further maps the segmented utterance into certain phonemes or Hidden Markov Model (HMM) states to complete the speech recognition process. The accuracy of the speech recognition process is, thus, dependent on the segmentation performed by the speech recognizer.

[003] Hidden Markov Models (HMMs) are conventionally used to model phonetic units. During conventional HMM expectation maximization (EM) training, HMM models are updated to increase the likelihood of training data. Usually the segmentation of the speech utterances also improves over each iteration of training. Due to a number of reasons, such as, for example, obtaining a poor initial model and the independence assumption with the HMM, segmentation using HMM implicitly during training and subsequent recognition can be poor. Based on the segmentation, the conventional HMM decoder computes phoneme recognition scores that are used to recognize the input speech utterances. The poor segmentation achieved with convention HMM decoders, therefore, has a significant negative impact on the

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[004] As a result, there exists a need for a system and method that improves the segmentation of speech utterances in a speech recognition system.

SUMMARY OF THE INVENTION

[005] Systems and methods consistent with the present invention address this and other needs by providing mechanisms that facilitate the segmentation of acoustic data input into a speech recognition system. For example, cepstral coefficients obtained from the speech recognition system front end can be used to improve the segmentation of acoustic data, thus, improving the accuracy of the speech recognition system.

[006] In accordance with the purpose of the invention as embodied and broadly described herein, a method of segmenting acoustic data for use in a speech recognition process includes receiving frames of acoustic data, determining cepstral coefficients for each of the received frames of acoustic data, and segmenting the received frames of acoustic data based on the determined cepstral coefficients.

[007] In another implementation consistent with the present invention, a method of recognizing patterns in acoustic data includes receiving frames of acoustic data, determining segmentation information corresponding to the received frames of acoustic data, determining at least one weighting parameter based on the determined segmentation information, and recognizing patterns in the received frames of acoustic data using the at least one weighting parameter.

20 [008] In a further implementation consistent with the present invention, a method of recognizing patterns in acoustic data includes receiving frames of acoustic data, determining first segmentation information corresponding to the received frames of acoustic data, determining second segmentation information corresponding to the received frames of

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acoustic data, determining at least one weighting parameter based on the determined second segmentation information, and recognizing patterns in the received frames of acoustic data using the at least one weighting parameter.

BRIEF DESCRIPTION OF THE DRAWINGS

- 5 [009] The accompanying drawings, which are incorporated in and constitute a part of this specification, illustrate an embodiment of the invention and, together with the description, explain the invention. In the drawings,
 - [0010] FIG. 1 illustrates an exemplary speech recognition device in which a system and method, consistent with the present invention, may be implemented;
 - [0011] FIG. 2 illustrates a plot of cepstral coefficients for a frame of audio data consistent with the present invention;
 - [0012] FIGS. 3-6 are flowcharts that illustrate exemplary processing for obtaining cepstra based HMM state/phoneme segmentation consistent with the present invention;
 - [0013] FIGS. 7-8 are flowcharts that illustrate exemplary processing for weighting trainer/HMM decoder output scoring, using cepstra-based HMM state/phoneme segmentation, consistent with the present invention; and
 - [0014] FIGS. 9-10 are flowcharts that illustrate exemplary system processing for weighting HMM decoder recognition hypothesis scores, using cepstra based HMM state/phoneme segmentation, consistent with the present invention.

DETAILED DESCRIPTION

[0015] The following detailed description of the invention refers to the accompanying drawings. The same reference numbers in different drawings identify the same or similar

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elements. Also, the following detailed description does not limit the invention. Instead, the scope of the invention is defined by the appended claims.

[0016] Systems and methods, consistent with the present invention, provide mechanisms that improve segmentation in a speech recognition system. Acoustic data received by the speech recognition system may be separated into frames by the speech recognition system front end. The speech recognition system may divide the frames into sets, such that each set contains a phoneme or HMM state. The speech recognition system may identify the end frame number for each frame set and use the end frame number either during or after HMM decoding. In an implementation consistent with the present invention, the speech recognition system uses the cepstral coefficient peaks for each frame of received acoustic data as a basis for segmenting the acoustic data for improved speech recognition processing.

[0017] EXEMPLARY ACOUSTIC RECOGNITION DEVICE

[0018] FIG. 1 illustrates an exemplary speech recognition device 105 in which a system and method, consistent with the present invention, may be implemented to improve speech recognition accuracy. Speech recognition device 105 may include an acoustic input device 110, an acoustic front end 115, a Hidden Markov Model (HMM) decoder 120, a processing unit 125, a memory 130, and a bus 135.

[0019] Acoustic input device 110 may include conventional circuitry for sampling and converting analog acoustic input signals into digital signal form. For example, acoustic input device 110 may include a microphone (not shown) that converts acoustic input signals into analog electrical signals, and an analog-to-digital converter (not shown) for sampling the analog electrical signals and converting the signals from analog to digital signal form.

[0020] Acoustic front end 115 may include conventional circuitry for pre-processing the

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digital acoustic signal received from acoustic input device 110. Acoustic front end 115 receives the digital signal from acoustic input device 110 and, in accordance with conventional techniques, processes the signal to generate frame-based cepstra data. The frame-based cepstra data can include, for example, fifteen cepstral coefficients per frame.

- Acoustic front end 115 sends the frame-based cepstra data to either trainer/HMM decoder 120 or processing unit 125.
 - [0021] Trainer/HMM decoder 120 can perform speech recognition processing using conventional Hidden Markov Models and conventional expectation maximization (EM) model training techniques. Trainer/HMM decoder 120 may perform segmentation processing that divides the acoustic input signal into stationary segments in accordance with conventional techniques. Trainer/HMM decoder 120 may further perform recognition processing that includes mapping the segmented acoustic input signal into certain phonemes or phoneme HMM states using conventional HMM techniques. Trainer/HMM decoder 120 may be implemented in hardware or as a sequence of instructions for execution in a processing unit, such as processing unit 125.
 - [0022] Processing unit 125 may perform functions for processing data received from acoustic front end 115 and/or trainer/HMM decoder 120. Memory 130 provides permanent and/or semi-permanent storage of data and instructions for use by processing unit 125.

 Memory 130 may include large-capacity storage devices, such as a magnetic and/or optical recording medium and its corresponding drive. Bus 135 interconnects the various components of speech recognition device 105 to permit the components to communicate with one another.

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[0023] The configuration of components of speech recognition device 105 illustrated in FIG. 1 is for illustrative purposes only. One skilled in the art will recognize that other configurations may be implemented.

[0024] EXEMPLARY CEPSTRA-BASED SEGMENTATION PROCESSING

[0025] FIG. 2 illustrates a graph 200 of cepstral coefficient data for a frame of acoustic data processed by acoustic front end 115. Graph 200 plots cepstral coefficient magnitude 205 on the Y-axis versus cepstral coefficient sequence number 210 on the X-axis. As can be seen in FIG. 2, a plot of the magnitudes of the cepstral coefficients for a frame of acoustic data forms a curve. The curve may include local maxima, or peaks, such as peaks 215, 220 and 225. For each frame of cepstral coefficient data, the number of coefficient peaks may be different. The number of cepstral coefficient peaks can be computed for each frame, thus, obtaining a sequence of cepstral coefficient peak numbers indexed by frame number.

- [0026] The number of cepstral coefficient peaks change rapidly on frames corresponding to phoneme boundaries. Additionally, within each phoneme segment, there may be other frames where the number of cepstral coefficient peaks changes quickly. These frames further divide the original phoneme segment into smaller segments. These fragments can be considered HMM state segments.
- 20 [0027] FIGS. 3-6 are flowcharts that illustrate exemplary processing, consistent with the present invention, for providing cepstra-based segmentation of acoustic data. As one skilled in the art will appreciate, the method exemplified by FIGS. 3-6 can be implemented as a sequence of instructions and stored in a computer-readable medium, such as memory 130 of speech recognition device 105, for execution by processing unit 125. A computer-readable

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medium may include one or more memory devices and/or carrier waves. Alternatively, the process may be implemented in hardware or in any combination of hardware and software.

[0028] Generally, the method exemplified by FIGS. 3-6 generates the end frame number for each phoneme or HMM state using cepstra coefficient data and either uses the end frame number during processing by the trainer/HMM decoder 120 (FIGS. 7 and 8) or after processing by the HMM decoder 120 (FIGS. 9 and 10). In other implementations consistent with the present invention, other processes may be used to generate the end frame number for each phoneme or HMM state. In this case, the processing described below with regard to FIGS. 7-8 and 9-10 can be used or may be modified as appropriate to operate upon the end frame number.

[0029] To begin processing, processing unit 125 sets an array of segmentation information (seg_info(x)), stored in memory 130, to initialized values of -2 [step 305]. Processing unit 125 may further initialize a frame index counter *i* by setting *i* to zero [step 310]. Processing unit 125 may then receive cepstra data ceps_data(frame_i) corresponding to the current frame (frame_i) from acoustic front end 115 [step 315]. Processing unit 125 can compute the number of cepstral peaks peak_num(ceps_data(frame_i)) in frame *i* using, for example, a graph of cepstral coefficient data such as graph 200 [step 320]. After computation of the number of cepstral peaks for frame_i, processing unit 125 may store peak_num(ceps_data(frame_i)) in memory 130 [step 325].

20 [0030] If the frame index counter *i* is currently set to zero [step 330], then processing unit 125 can set the segmentation information corresponding to frame_0 (seg_info(frame_0)) to zero [step 335] and then may proceed with step 605 (FIG. 6). If *i* is not equal to zero, then processing unit 125 may optionally continue with either a first technique or a second

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technique. In the first technique, beginning at step 405 (FIG. 4), processing unit 125 may determine if the number of cepstral peaks for the current frame ($frame_i$) subtracted from the number of cepstral peaks for the previous frame ($frame_i$ -1) is greater than a value δ , and further if segmentation information determined for frame $frame_i$ -2 is not equal to 1, as shown in the following relations (Eqn. (1)):

$$peak_num(ceps_data(frame_i-1) - peak_num(ceps_data(frame_i)) > \delta$$
 and
$$seg_info(frame_i-2) \neq 1$$

[0031] If these conditions are satisfied, then processing unit 125 can set the segmentation information for the previous frame (seg_info(frame_i-1)) to 1 [step 415] and the segmentation information for the current frame (seg_info(frame_i)) to zero [step 420]. Otherwise, processing unit 125 may set the segmentation for the previous frame (seg_info(frame_i-1)) to -1 [step 410]. Processing may then continue with step 605 (FIG. 6).

[0032] In the second technique, beginning at step 505 (FIG. 5), processing unit 125 may determine if the number of cepstral peaks for the current frame (frame_i) subtracted from the number of cepstral peaks for a jth frame (frame_j) is greater than a value δ, and further if the number of cepstral peaks for an xth frame (frame_x) is greater than, or equal to, a number of cepstral peaks for an (x+1)th frame (frame_x+1), as shown in following relations (Eqn. (2)):

 $peak_num(ceps_data(frame_j)) - peak_num(ceps_data(frame_i)) > \delta$ 20 and $peak_num(ceps_data(frame_x)) \ge peak_num(ceps_data(frame_x+I))$

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where $frame_j \le frame_x < frame_i$

[0033] If these conditions are satisfied, than processing continues with step 520. If not, processing unit 125 may determine if the number of cepstral peaks for the current frame (frame_i) subtracted from the number of cepstral peaks for a j^{th} frame (frame_j) is less than a value δ , and further if the number of cepstral peaks for an x^{th} frame (frame_x) is less than, or equal to, a number of cepstral peaks for an $(x+1)^{th}$ frame (frame_x+1), as shown in the following relations (Eqn. (3)):

 $peak_num(ceps_data(frame_j)) - peak_num(ceps_data(frame_i)) < \delta$ and $peak_num(ceps_data(frame_x)) \le peak_num(ceps_data(frame_x+I))$ where $frame_j \le frame_x < frame_I$

[0034] If these conditions are satisfied, processing unit 125 may set the segmentation information for the previous frame (seg_info(frame_i-1)) to 1 [step 520] and the segmentation information for the current frame (seg_info(frame_i)) to zero [step 525]. Otherwise,

processing unit 125 may set the segmentation information for the previous frame (seg_info(frame_i-l)) to -1 [step 515]. Subsequent to steps 515 or 525, processing continues with step 605.

[0035] At step 605, processing unit 125 determines if the current frame (frame_i) is the last frame. If not, processing unit 125 increments the frame index i [step 610] and returns to step 315 (FIG. 3). If the current frame is the last frame, processing unit 125 sets the

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EXPRESS MAIL NO. EK673491182US

PATENT DOCKET NO. 00-4023

segmentation information for the current frame (seg_info(frame_i)) to 1 [step 615] and processing completes.

[0036] EXEMPLARY TRAINER/HMM DECODER OUTPUT SCORE PROCESSING

[0037] FIGS. 7-8 are flowcharts that illustrate exemplary processing, consistent with the present invention, for weighting trainer/HMM decoder output scores. The method exemplified by FIGS. 7-8 may operate upon the end frame numbers identified using cepstra coefficient data, as described above. In other implementations consistent with the present invention, the end frame numbers are determined using other techniques. Additionally, the method exemplified by FIGS. 7-8 may be implemented within the training processes of trainer/HMM decoder 120.

[0038] As one skilled in the art will appreciate, the method exemplified by FIGS. 7-8 can be implemented as a sequence of instructions and stored within an internal memory (not shown) of trainer/HMM decoder 120 for execution by trainer/HMM decoder 120.

Alternatively, the process may be implemented in hardware, or in any combination of hardware and software, within trainer/HMM decoder 120. Furthermore, the process may be implemented as a sequence of instructions and stored within memory 130 of speech recognition device 105 for execution by processing unit 125.

20 [0039] To begin processing, trainer/HMM decoder 120 may determine a conventional output score according to the conventionally determined phonemes or HMM states [step 705]. Trainer/HMM decoder 120 can then set a frame index value i to zero [step 710]. Trainer/HMM decoder 120 can receive the previously determined segmentation information (determined in the processing of FIGS. 3-6) from memory 130 [step 715]. At step 720,

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PATENT DOCKET NO. 00-4023

trainer/HMM decoder 120 determines if the segmentation information for the current frame $(seg_info(frame_i))$ equals one. If not, trainer/HMM decoder 120 sets a weight value for the current frame $(weight(seg_info(frame_i)))$ to one [step 725]. If so, trainer/HMM decoder 120 sets the weight value for the current frame to a constant α , where $\alpha > 1$ [step 730].

5 [0040] Trainer/HMM decoder 120 may then determine an output score corresponding to the current frame and to a HMM state [step 805] (FIG. 8) using the following relation:

[0041] Trainer/HMM decoder 120 may further determine an output score corresponding to the current frame and to a phoneme [step 810] using the following relation:

[0042] At step 815, trainer/HMM decoder 120 can determine if the current frame is the last frame. If not, trainer/HMM decoder 120 increments the frame index *i* [step 820] and returns to step 715 (FIG. 7). If the current frame is the last frame, speech recognition device 105 may complete the speech recognition process using the weighted output scores in accordance with conventional techniques [step 825].

[0043] EXEMPLARY HMM DECODER HYPOTHESIS SCORE PROCESSING

20 [0044] FIGS. 9-10 are flowcharts that illustrate exemplary processing, consistent with the present invention, for weighting HMM decoder hypothesis scores. The method exemplified

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EXPRESS MAIL NO. EK673491182US

PATENT DOCKET NO. 00-4023

by FIGS. 9-10 may operate upon the end frame numbers identified using cepstra coefficient data, as described above. In other implementations consistent with the present invention, the end frame numbers are determined using other techniques.

[0045] As one skilled in the art will appreciate, the process exemplified by FIGS. 9-10 can be implemented as a sequence of instructions stored within memory 130 of speech recognition device 105 for execution by processing unit 125. Alternatively, the process may be implemented in hardware or in any combination of hardware and software.

[0046] To begin processing, processing unit 125 may set a counter value j to zero [step 905]. Processing unit 125 then may receive a jth recognition hypothesis from HMM decoder 120 [step 910]. The received jth recognition hypothesis can include a hypothesis score (hyp_score_j) and hypothesis segmentation information $(hyp_seg_info_j)$ determined by HMM decoder 120 in accordance with conventional techniques. The hypothesis segmentation information $(hyp_seg_info_j)$ includes a set of conventional elements specifying frame numbers of frames that end a phoneme. For example, $hyp_seg_info_j = [ef_1, ef_2, ef_3, ef_4, ..., ef_{50}]$, where ef_n equals a frame number that ends a phoneme and n equals the index of each frame number. Processing unit 125 can then set a value k equal to zero [step 915]. Processing unit 125 may retrieve a kth frame number of the hypothesis segmentation information $(hyp_seg_info_j)$ and designate the kth frame number as a value k [step 920].

[0047] Processing unit 125 may further retrieve, from memory 130, the previously determined segmentation information (determined in the processing of FIGS. 3-6) for all values of y (seg_info(frame_y)) that satisfy the following relation:

$$|y-x| < \Delta$$
 Eqn. (6)

[step 925]. Processing unit 125 may determine if any of the retrieved y values of the

EXPRESS MAIL NO. EK673491182US

PATENT DOCKET NO. 00-4023

segmentation information is equal to 1 [step 930]. If not, processing proceeds with step 1015 (FIG. 10). If so, processing unit 125 may add a weight to the j^{th} hypothesis score [step 935] according to the following relation:

$$hyp_score_i = hyp_score_i + weight$$
 Eqn. (7)

- Processing unit 125 may then determine if k is equal to the index of the last frame number in the hypothesis segmentation information (hyp_seg_info_j) [step 940]. If k is not equal to the index of the last frame number, processing unit 125 increments k [step 945] and returns to step 920. If k is equal to the index of the last frame number, processing may proceed to step 1015 (FIG. 10).
- 10 [0049] At step 1015, processing unit 125 determines if all hypotheses received from HMM decoder 120 have been processed. If not, processing unit 125 increments *j* [step 1010] and returns to step 910 (FIG. 9). If all hypotheses have been processed, processing unit 125 re-orders the recognition hypotheses received from HMM decoder 120 based on the resulting hypothesis scores [step 1020] as possibly weighted by the processing of step 1005.
- Processing unit 125 may then complete the speech recognition process using the re-ordered recognition hypotheses in accordance with conventional techniques [step 1025].

[0050] CONCLUSION

[0051] Systems and methods, consistent with the present invention, provide mechanisms that improve segmentation in speech recognition systems using cepstral coefficients. A number of cepstral coefficient peaks for each frame of received acoustic data can be used, in a

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EXPRESS MAIL NO. EK673491182US

PATENT DOCKET NO. 00-4023

manner consistent with the present invention, to segment the acoustic data for improved speech recognition processing.

[0052] The foregoing description of exemplary embodiments of the present invention provides illustration and description, but is not intended to be exhaustive or to limit the invention to the precise form disclosed. Modifications and variations are possible in light of the above teachings or may be acquired from practice of the invention. For example, while certain components of the invention have been described as implemented in hardware and others in software, other configurations may be possible. Also, while series of steps have been described with regard to FIGS. 3-10, the order of the steps may be altered in other implementations. No element, step, or instruction used in the description of the present application should be construed as critical or essential to the invention unless explicitly described as such. The scope of the invention is defined by the following claims and their equivalents.